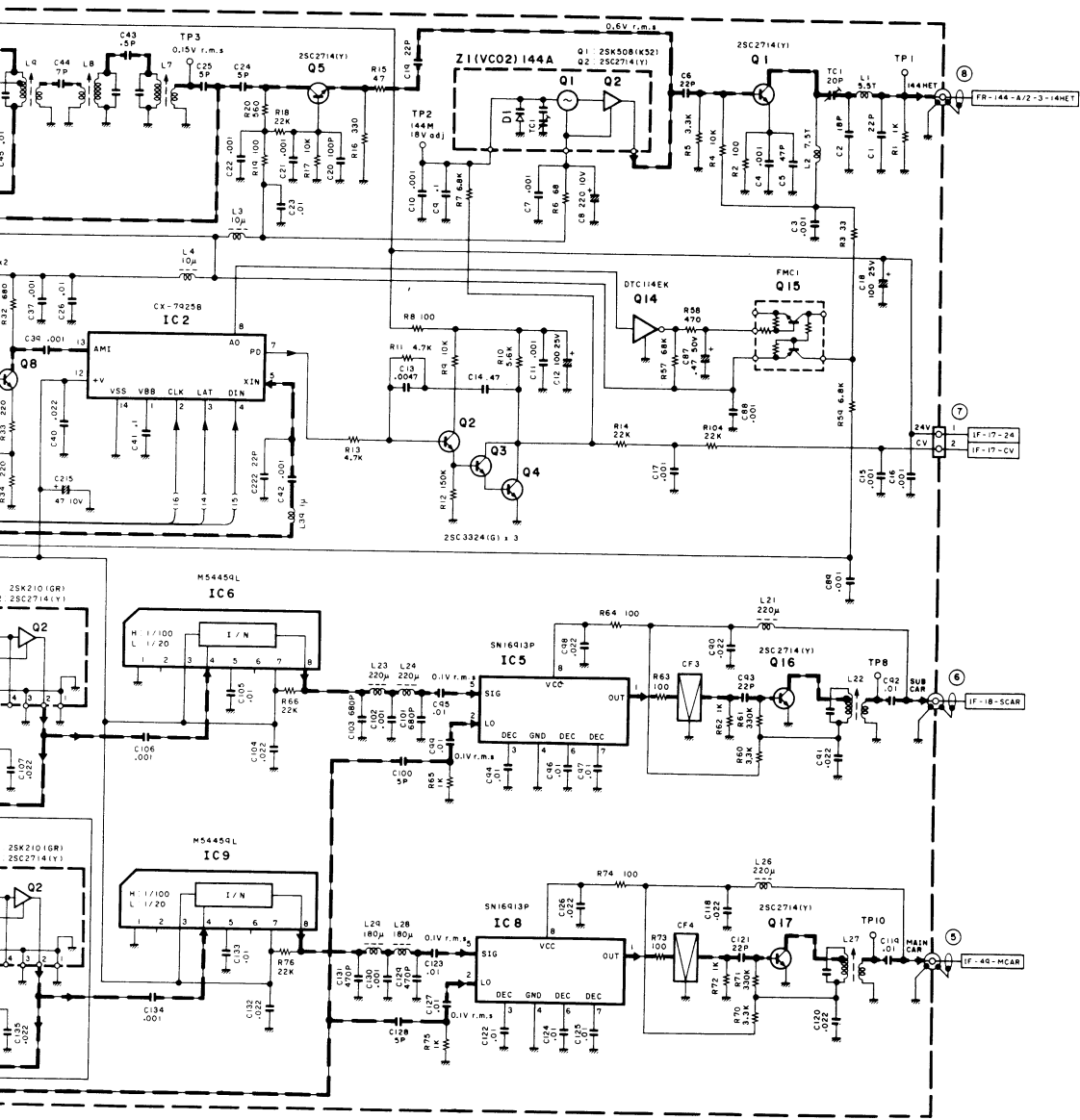
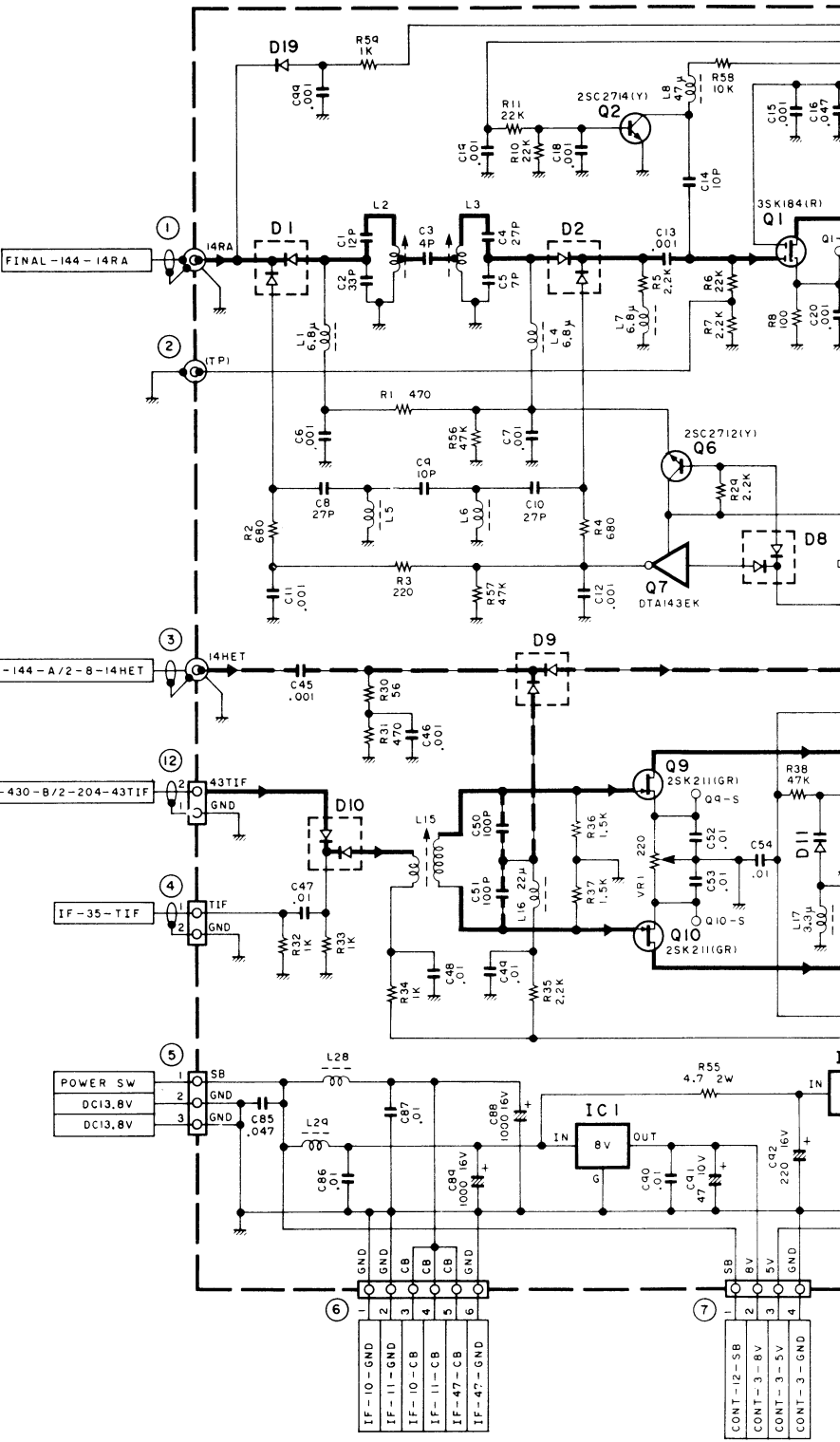


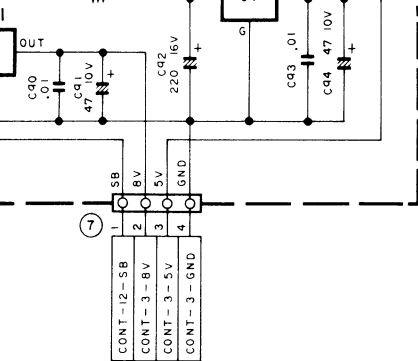
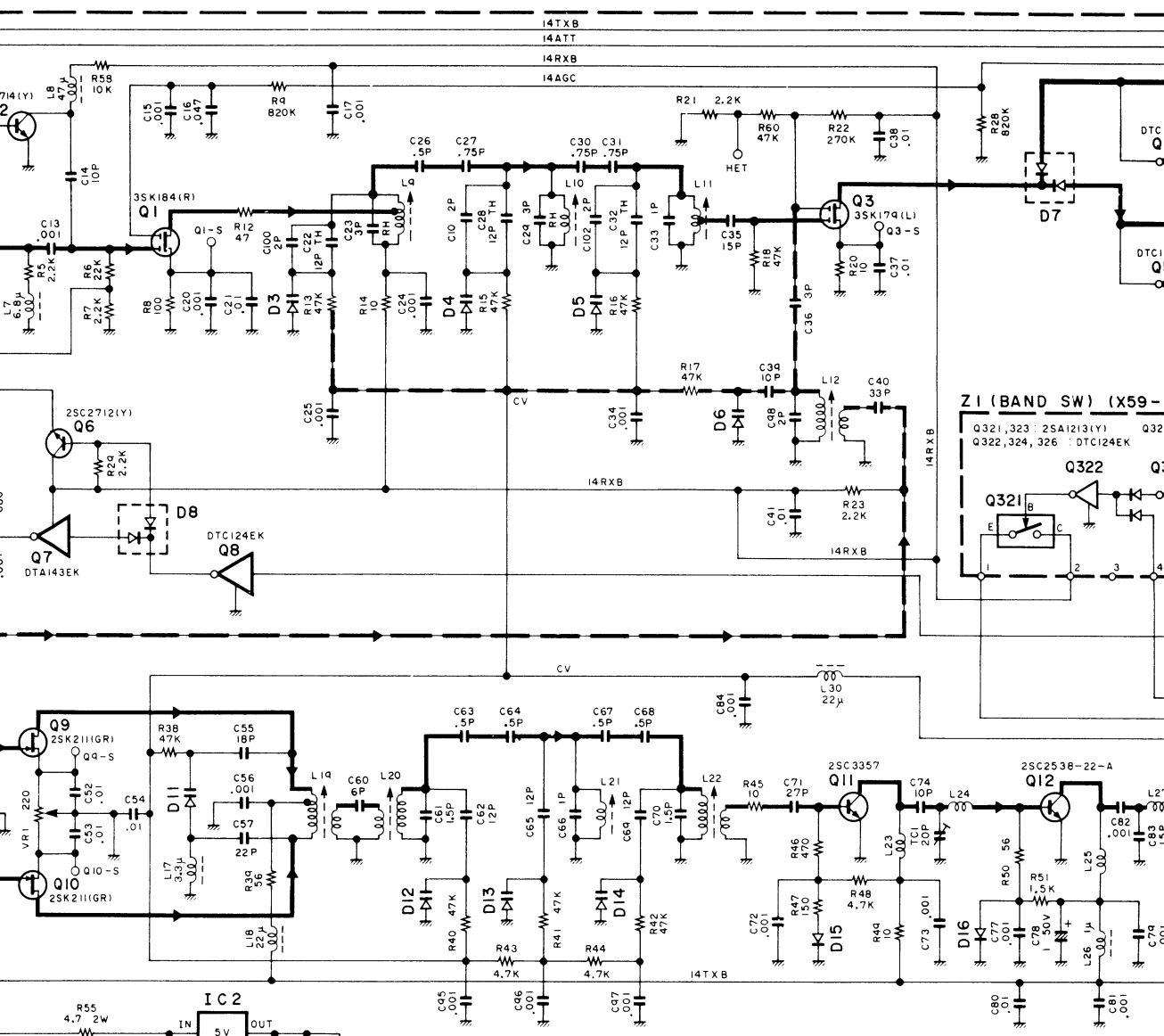
- | | | | | | | |
|---------------|------------|---------------------|------------|-------------|------------|----|
| IC1, 5, 8 | SN16913P | Q1, 5 ~ 8, 11 ~ 13, | 2SC2714(Y) | Q14, 27, 28 | DTC114EK | D2 |
| IC2, 4, 7, 10 | CX-7925B | 16, 17, 24, 25 | 2SC3324(G) | Q15 | FMC | D3 |
| IC3, 6, 9 | M54459L | Q2 ~ 4 | 2SC2714(Y) | Q18 ~ 23 | FMU1 | |
| IC11 | μPC780M05H | Q9, 10 | 2SK2101GR | Q26 | 2SA1213(Y) | |
| IC12 | TC4581F | | | | | |



- B : DTC114EK
- D2, 4 : ISV164
- FMC : FMC1
- D3, 5~7 : ISV166
- FMU1
- 2SA1213(Y)

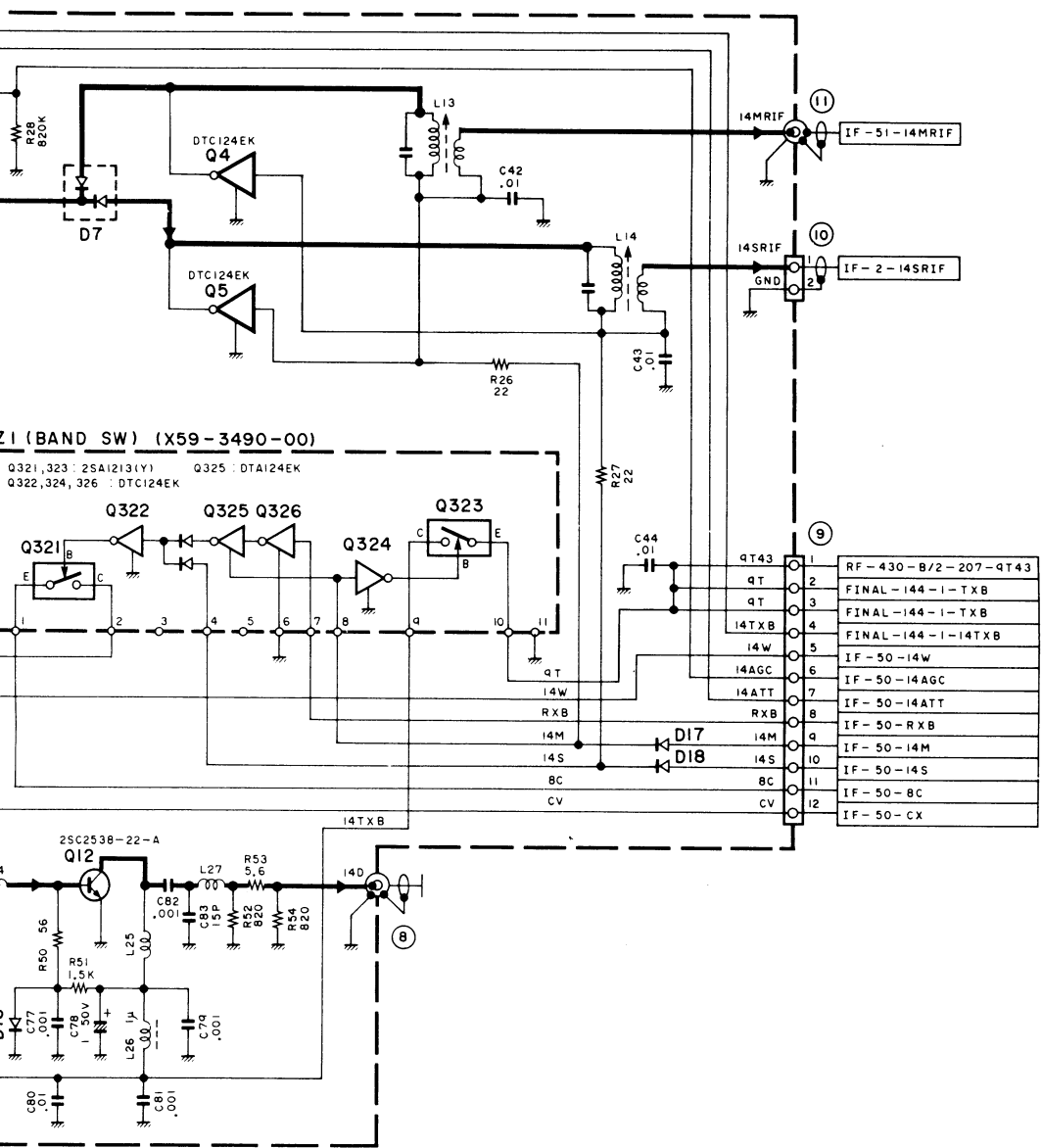
Note: _____
 Circuit Diagram is subject to change without notice due to advancements in technology.





- | | | | | | |
|-------|--------|------------|--------------|---------------|---------|
| IC1 : | UB7808 | Q1 : | 3SK184(R) | D1, 2, 7~10 : | DAN2538 |
| IC2 : | UB7805 | Q2 : | 2SC2714(Y) | D3~6, 11~14 : | ISV205 |
| | | Q3 : | 3SK179(L) | D15~18 : | RLS73 |
| | | Q4, 5, 8 : | DTC124EK | D19 : | RLS135 |
| | | Q6 : | 2SC2712(Y) | | |
| | | Q7 : | DTA143EK | | |
| | | Q9, 10 : | 2SK211(GR) | | |
| | | Q11 : | 2SC3357 | | |
| | | Q12 : | 2SC2538-22-A | | |

Note:
Circuit



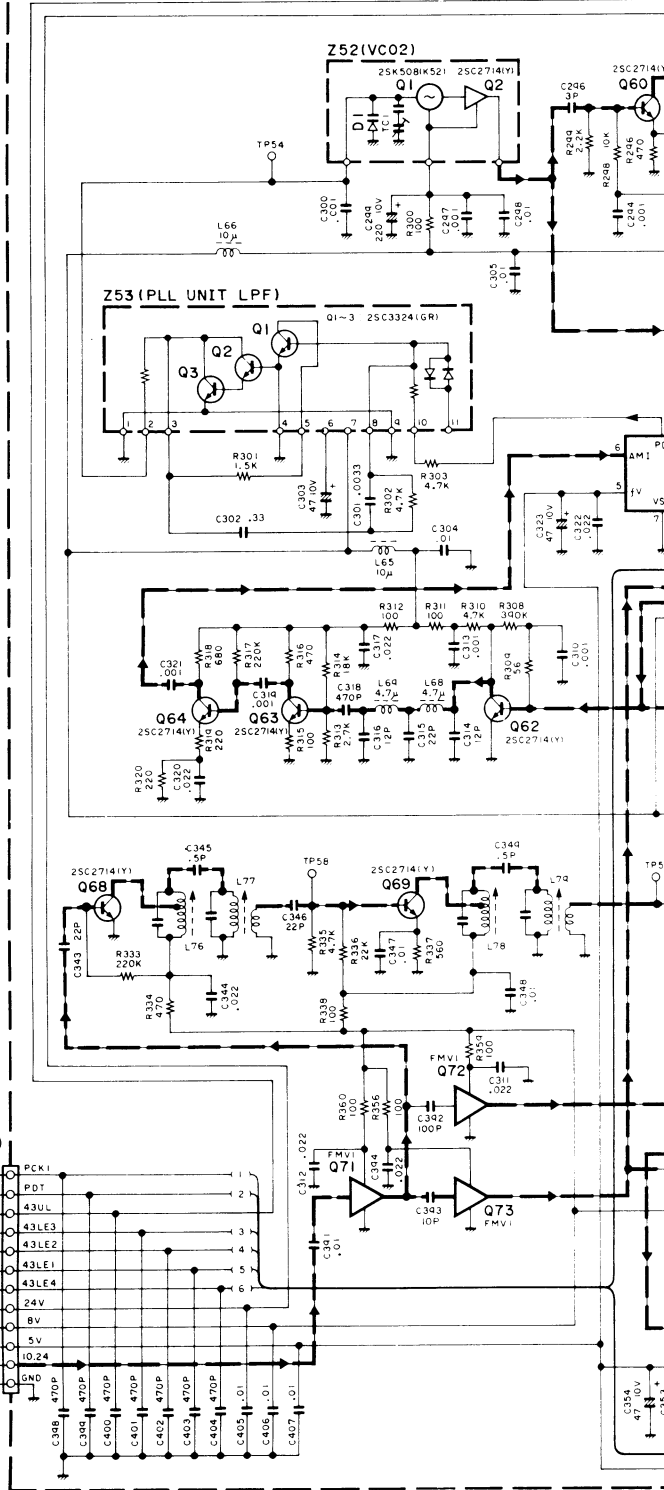
- D1, 2, 7~10 : DAN235K
- D3~6, 11~14 : ISV205
- D15~18 : RLS73
- D19 : RLS135

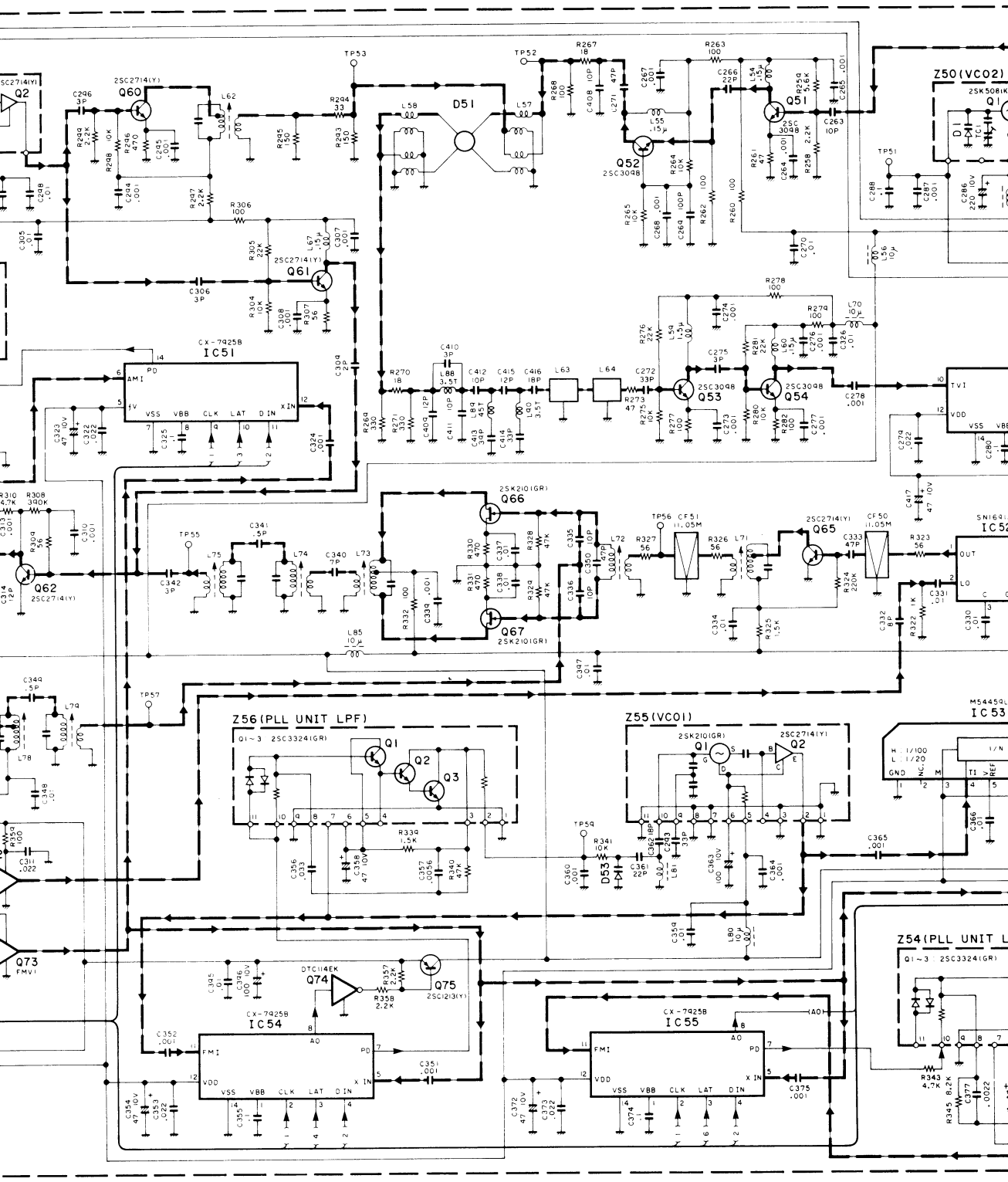
Note:
 Circuit Diagram is subject to change without notice due to advancements in technology.

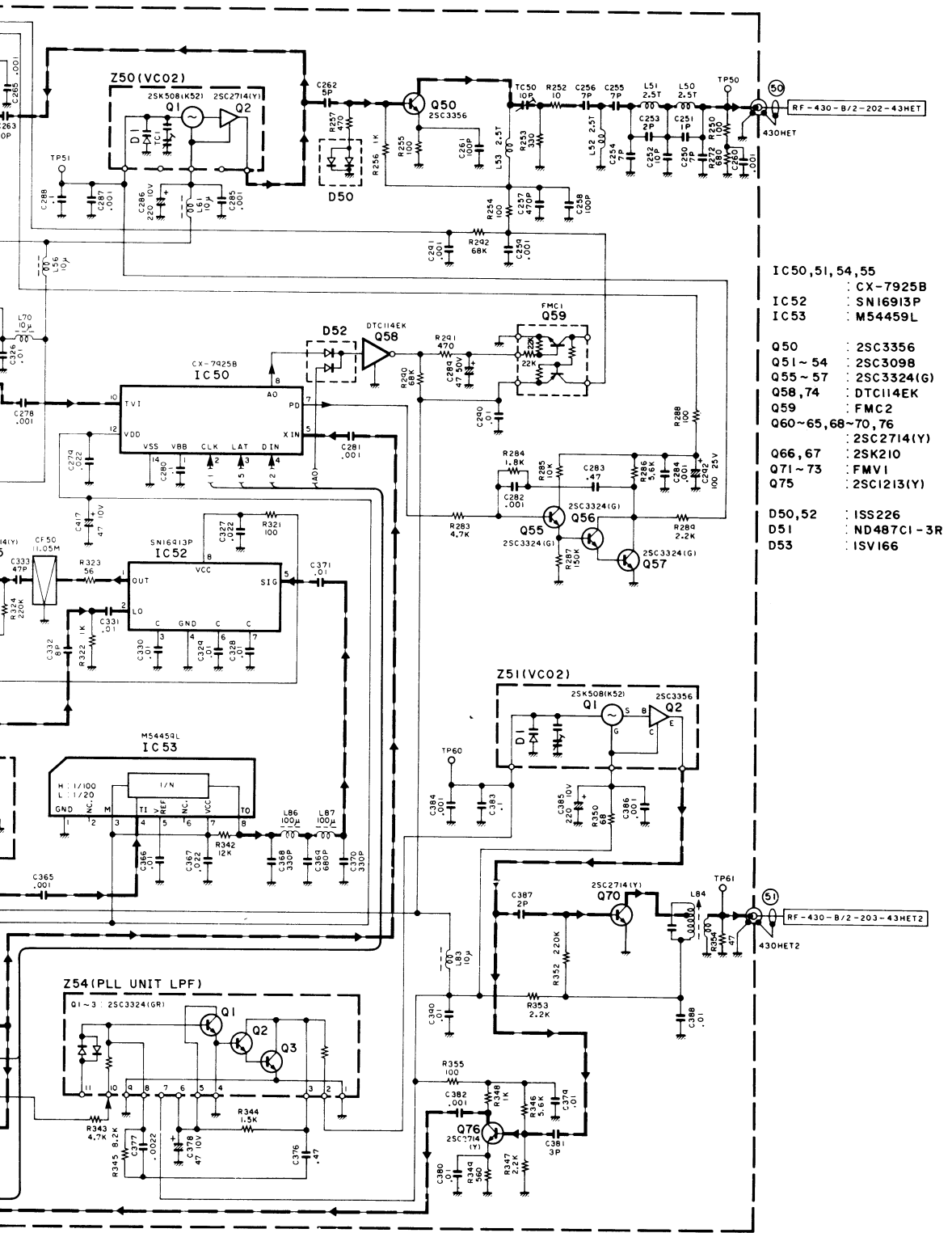
■ PLL (430 MHz) UNIT (X50-3080-00) (B/2)

PLL (430 MHz) UNIT
(X50-3080-00) (B/2)

1	PCK1
2	PDT
3	43UL
4	43LE3
5	43LE2
6	43LE1
7	43LE4
8	24V
9	8V
10	5V
11	10.24
12	SND

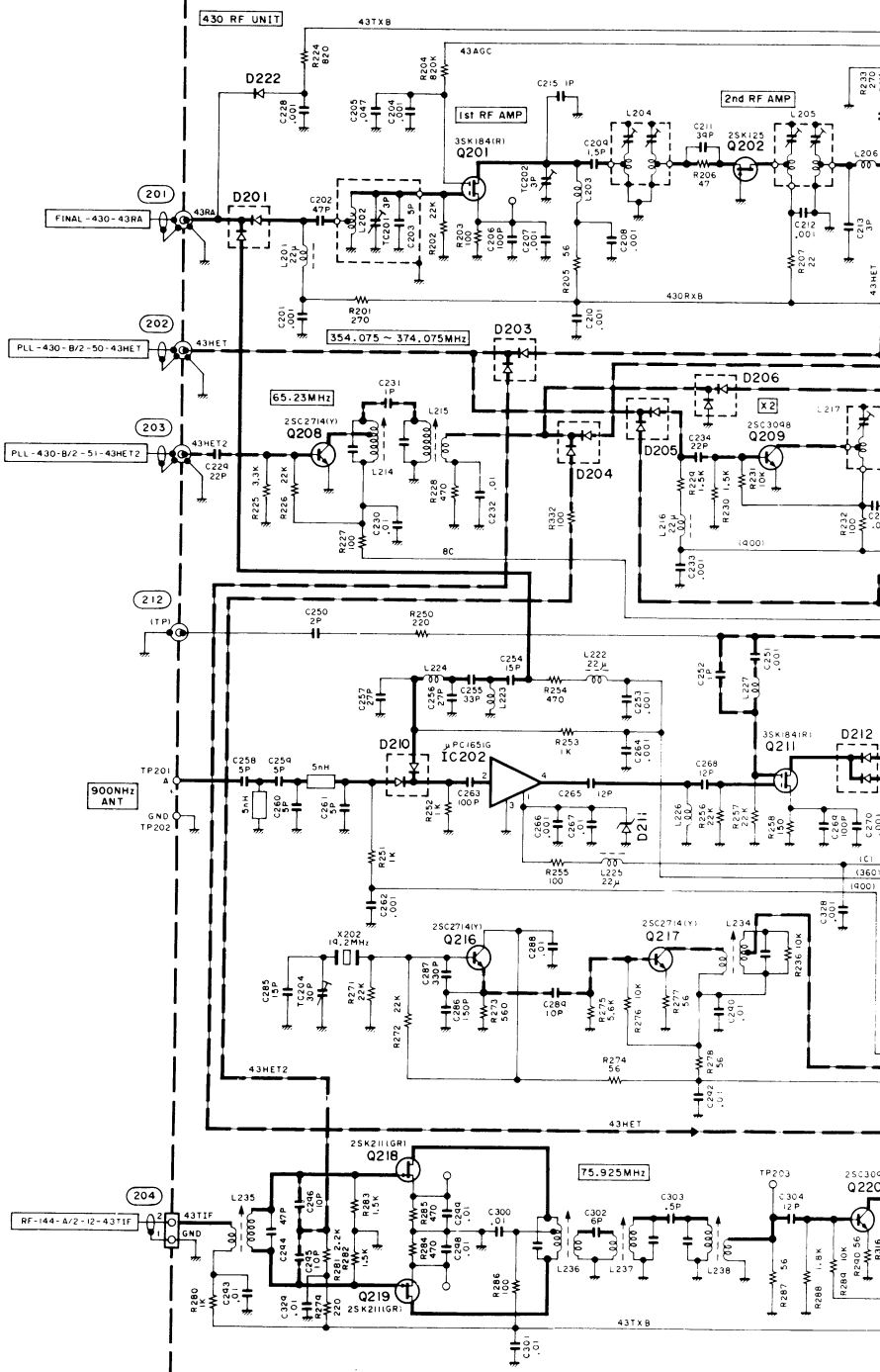


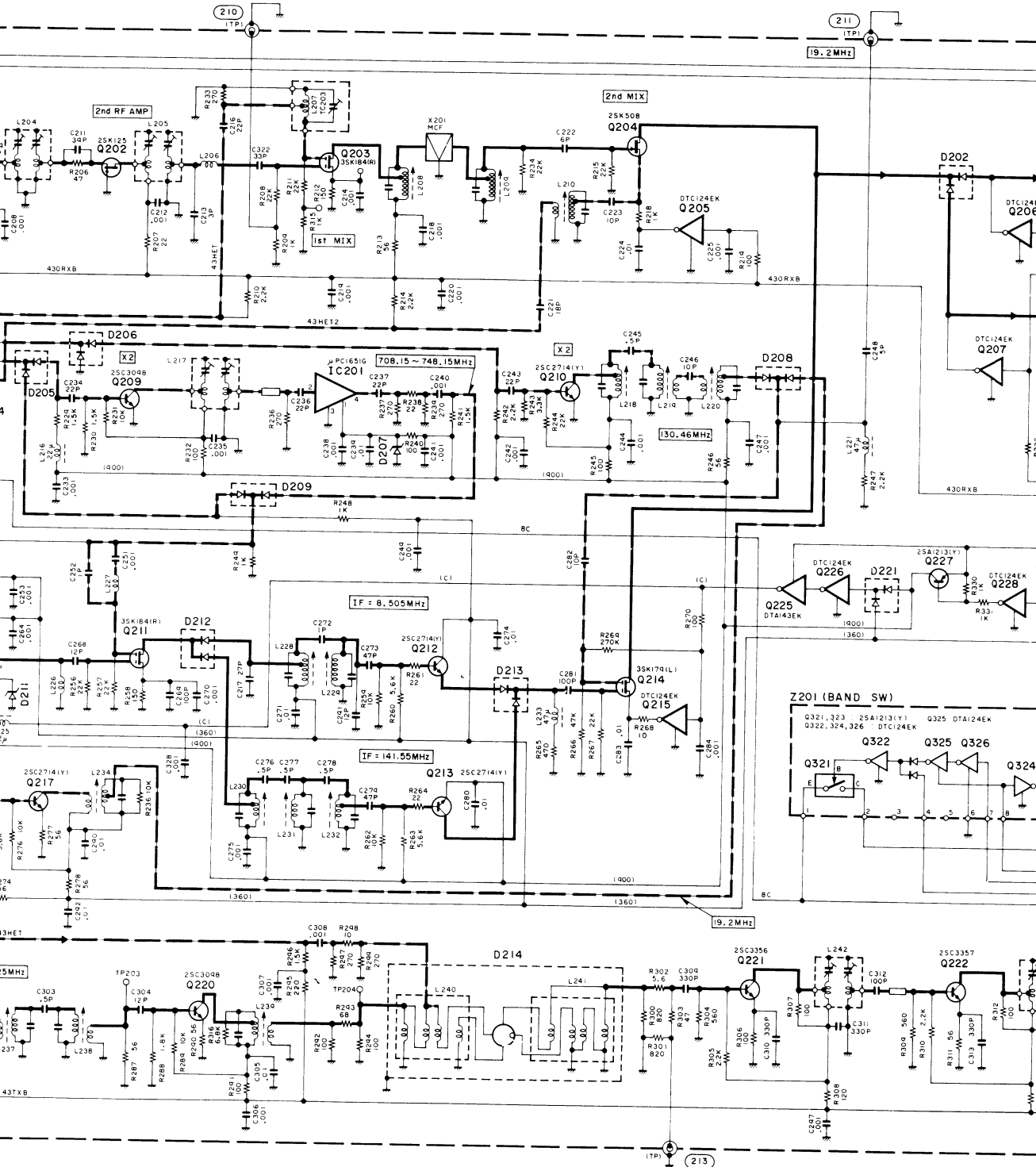




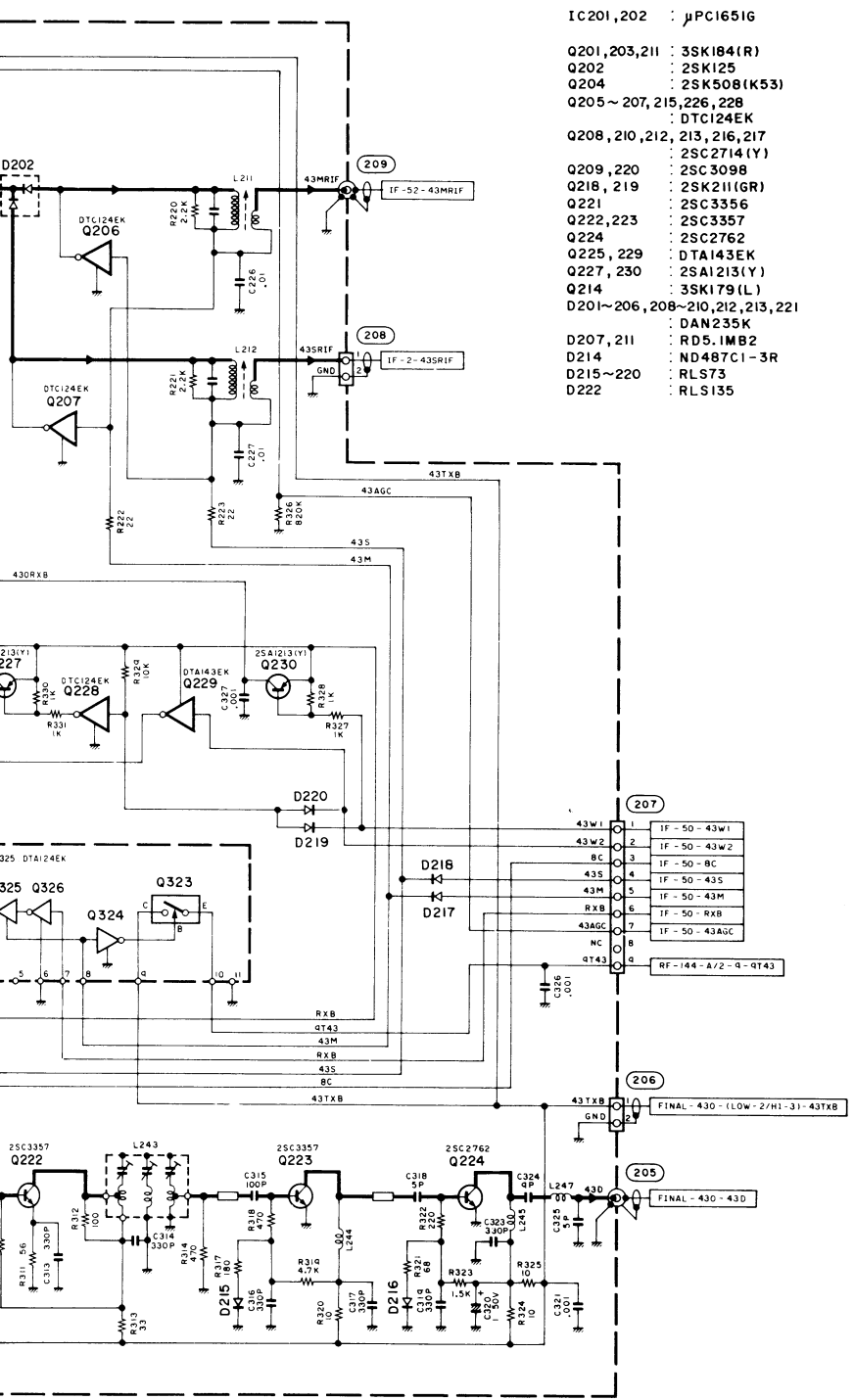
- IC 50, 51, 54, 55 : CX-7925B
- IC 52 : SN16913P
- IC 53 : M54459L
- Q50 : 2SC3356
- Q51 ~ 54 : 2SC3098
- Q55 ~ 57 : 2SC3324(G)
- Q58, 74 : DTC114EK
- Q59 : FMC2
- Q60 ~ 65, 68 ~ 70, 76 : 2SC2714(Y)
- Q66, 67 : 2SK210
- Q71 ~ 73 : FMV1
- Q75 : 2SC1213(Y)
- D50, 52 : ISS226
- D51 : ND487C1 - 3R
- D53 : ISV166

Note: Circuit Diagram is subject to change without notice due to advancements in technology.





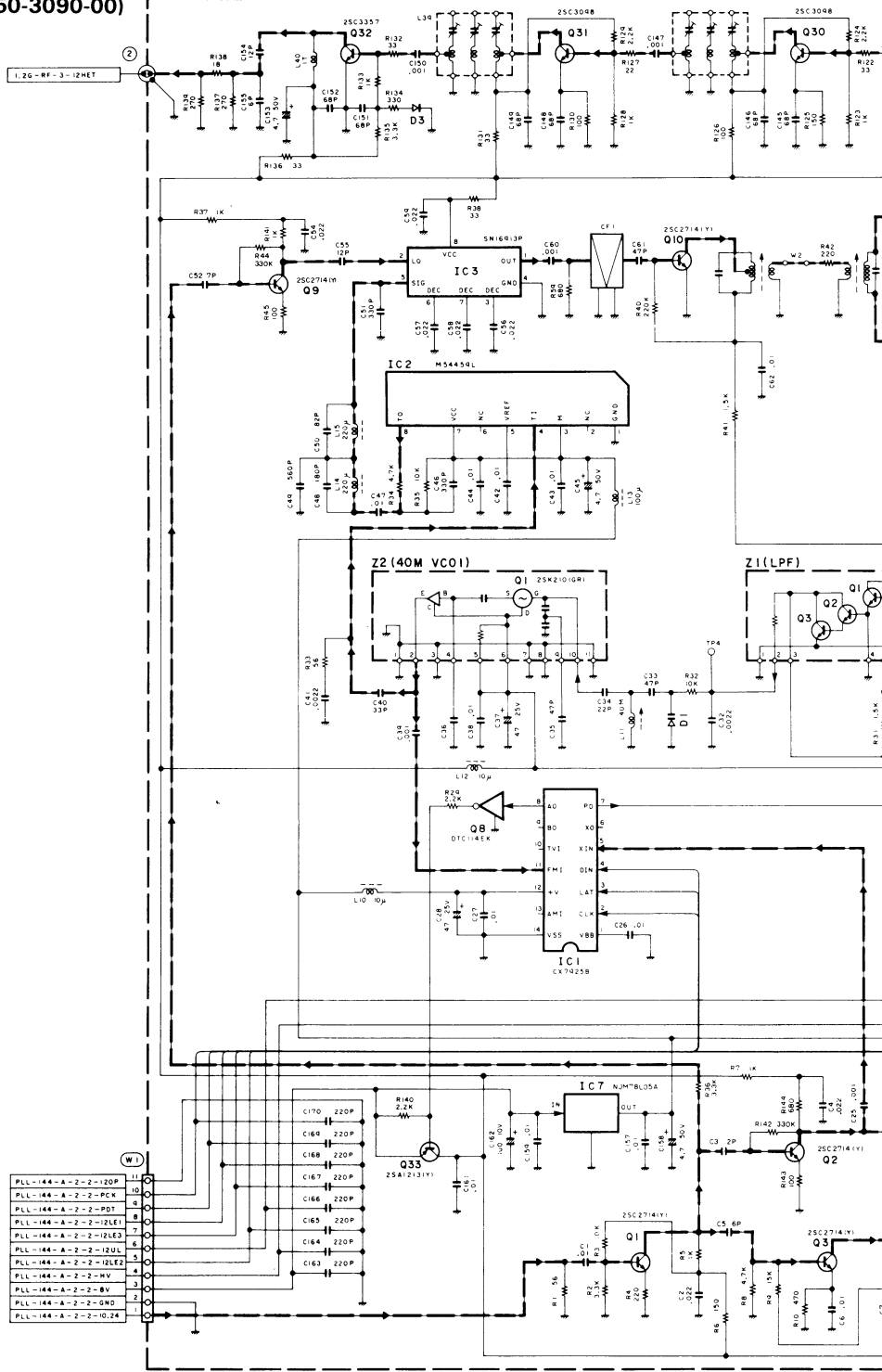
Note: Circuit Diagram is s

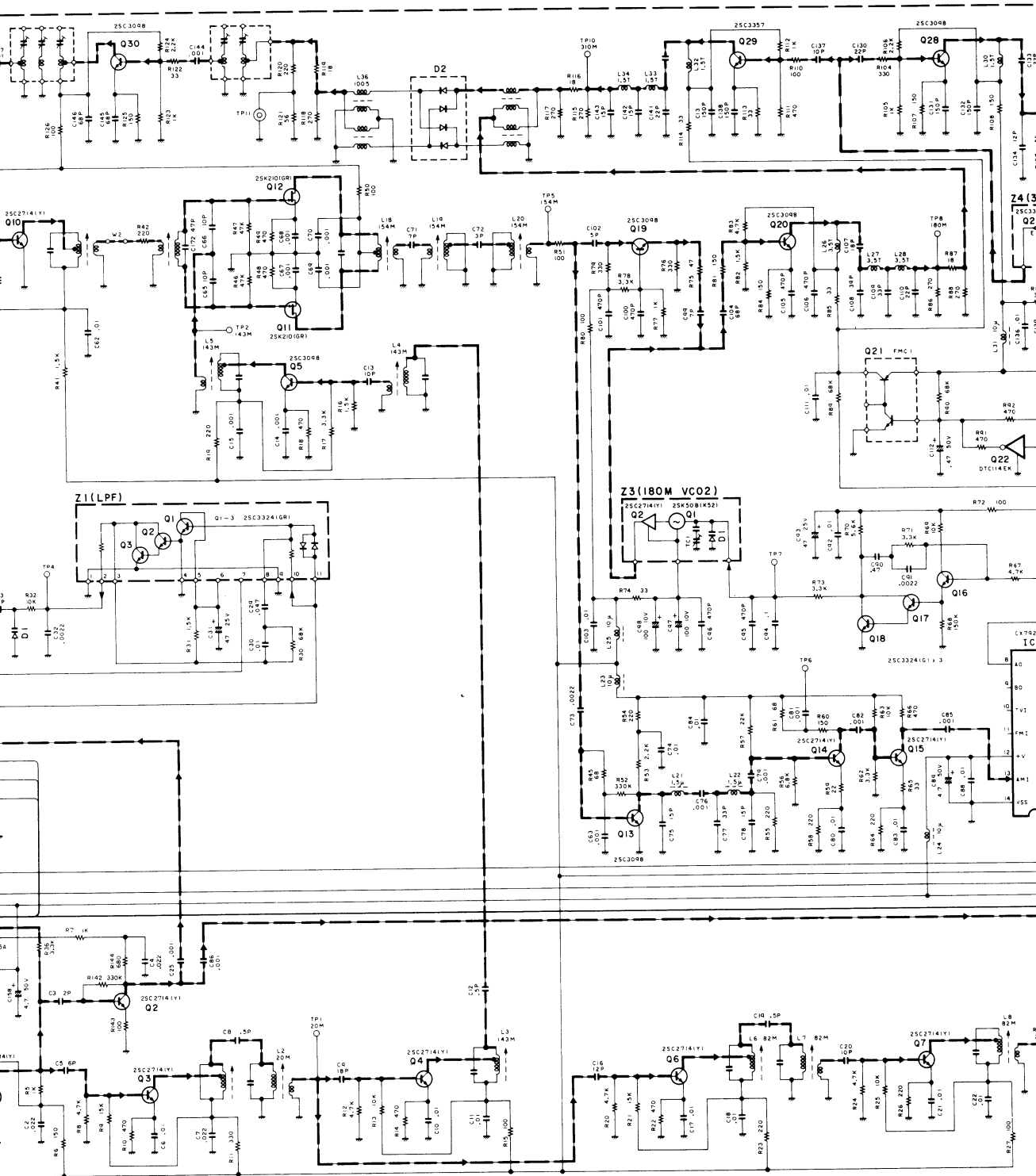


- IC201,202 : µPC16516
- Q201,203,211 : 3SK184(R)
 Q202 : 2SK125
 Q204 : 2SK508(K53)
 Q205~207,215,226,228 : DTC124EK
 Q208,210,212,213,216,217 : 2SC2714(Y)
 Q209,220 : 2SC3098
 Q218,219 : 2SK211(GR)
 Q221 : 2SC3356
 Q222,223 : 2SC3357
 Q224 : 2SC2762
 Q225,229 : DTA143EK
 Q227,230 : 2SA1213(Y)
 Q214 : 3SK179(L)
 D201~206,208~210,212,213,221 : DAN235K
 D207,211 : RD5.1MB2
 D214 : ND487C1-3R
 D215~220 : RLS73
 D222 : RLS135

PLL (1.2 GHz) UNIT (X50-3090-00)

PLL(1.2GHz) UNIT (X50-3090-00)





Note: Circuit Diagram is subject to change

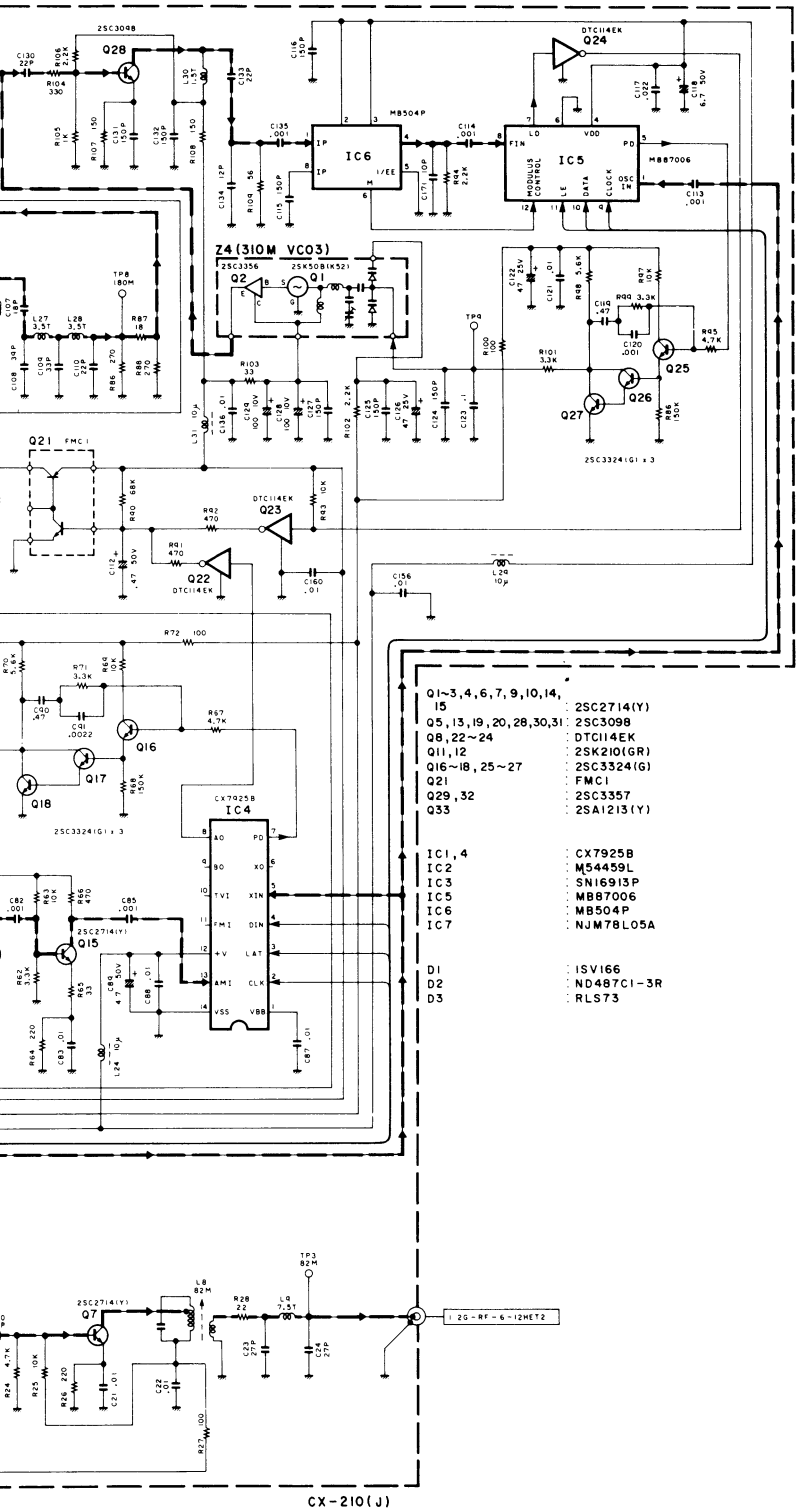


Diagram is subject to change without notice due to advancements in technology.

FINAL-1,2-W2-12RA

12RA

FINAL-1,2-2--6
FINAL-1,2-2-12RXB
FINAL-1,2-2-12TXB

12RXB
12TXB

FL-1,2-12HET

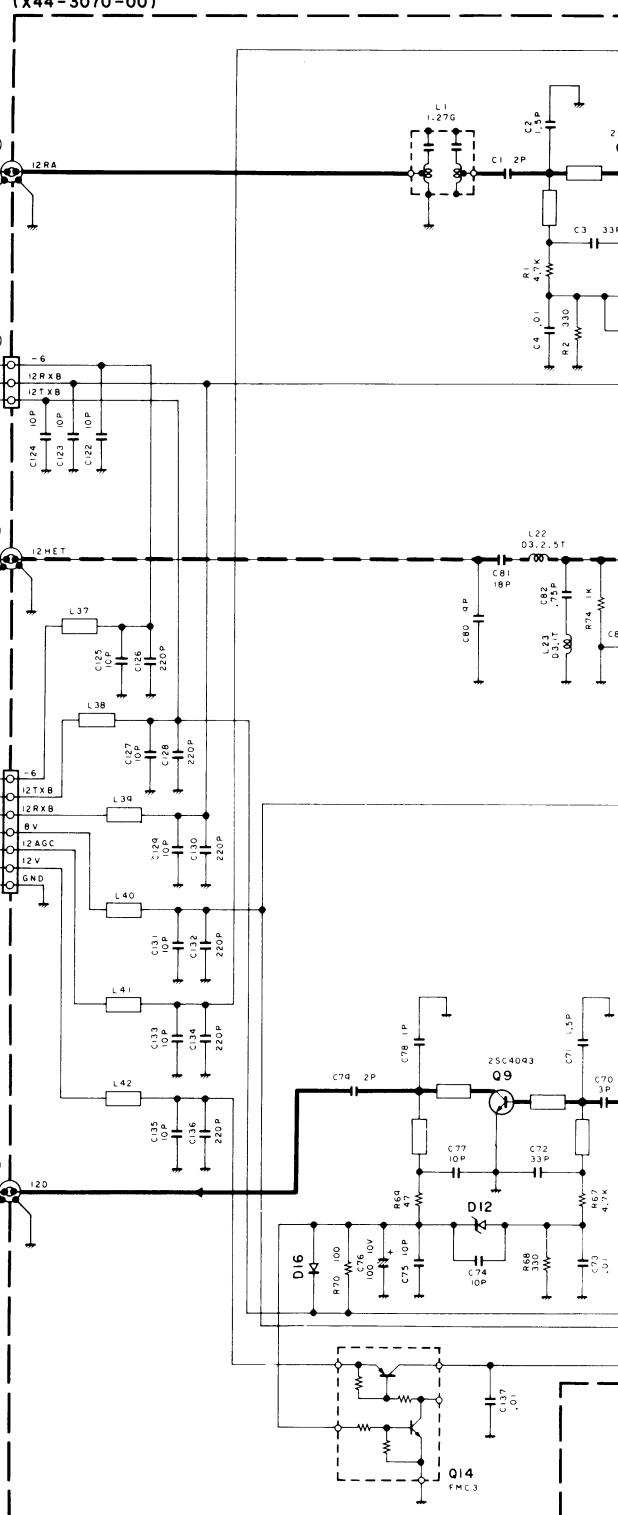
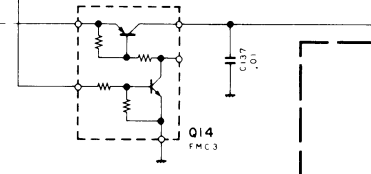
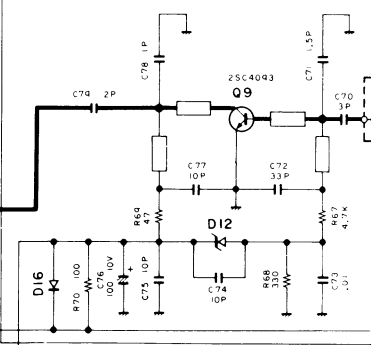
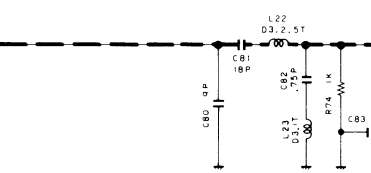
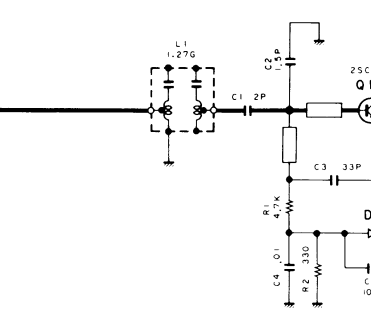
12HET

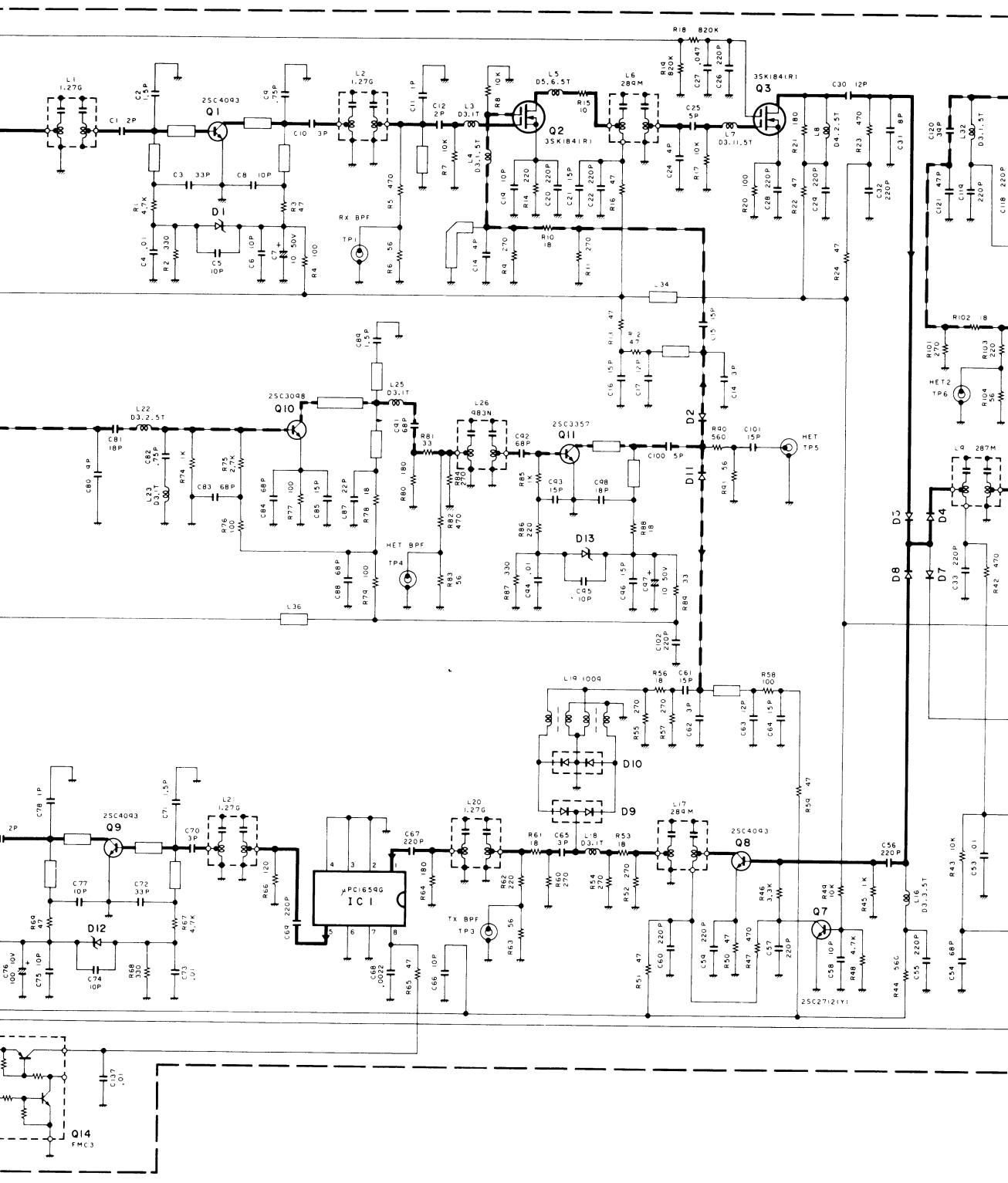
IF-32--6
IF-32-12TXB
IF-32-12RXB
IF-32-8V
IF-32-12AGC
FINAL-1,2,3-12V

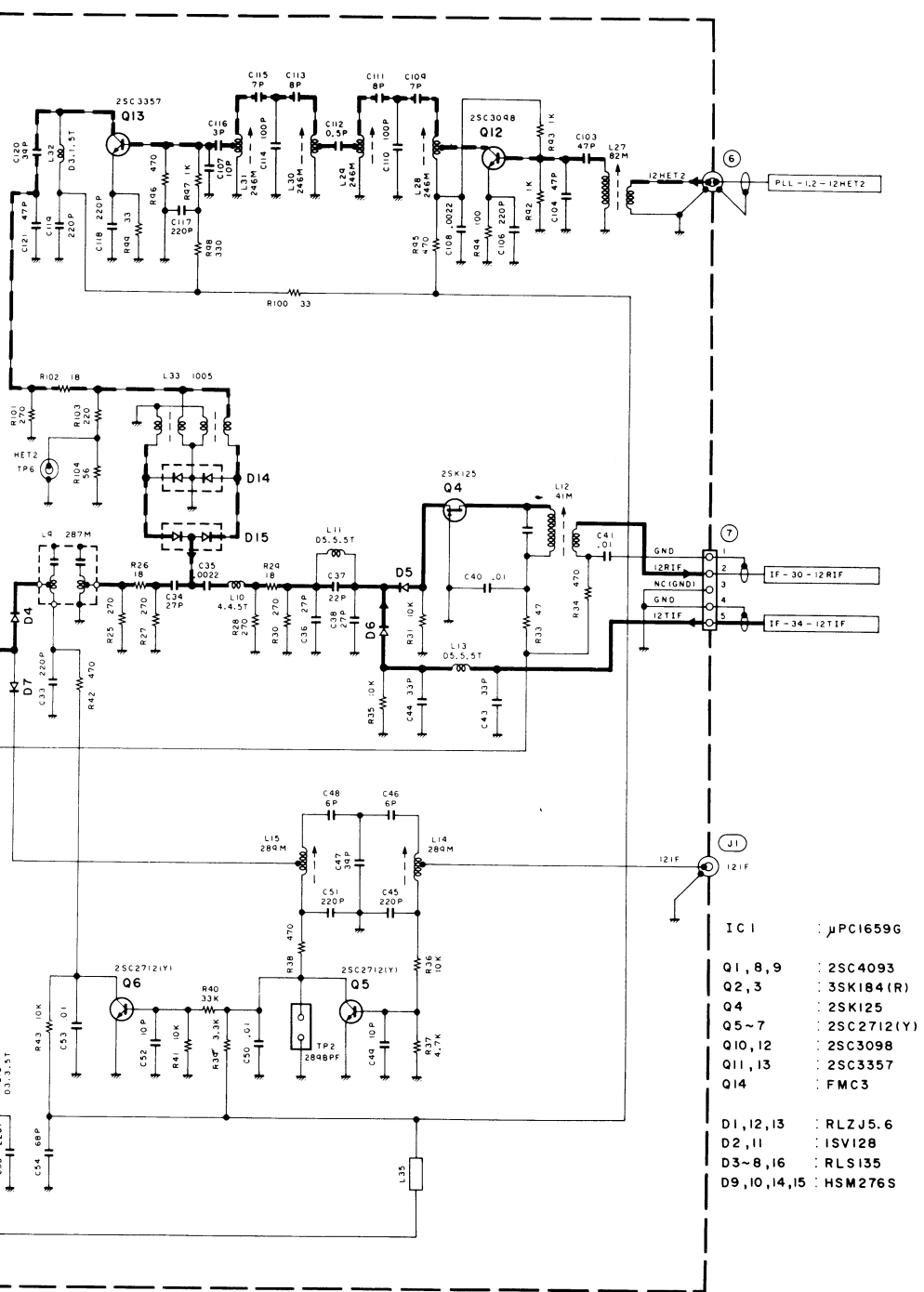
12TXB
12RXB
8V
12V
AGC
GND

FINAL-1,2-W1-12D

12D



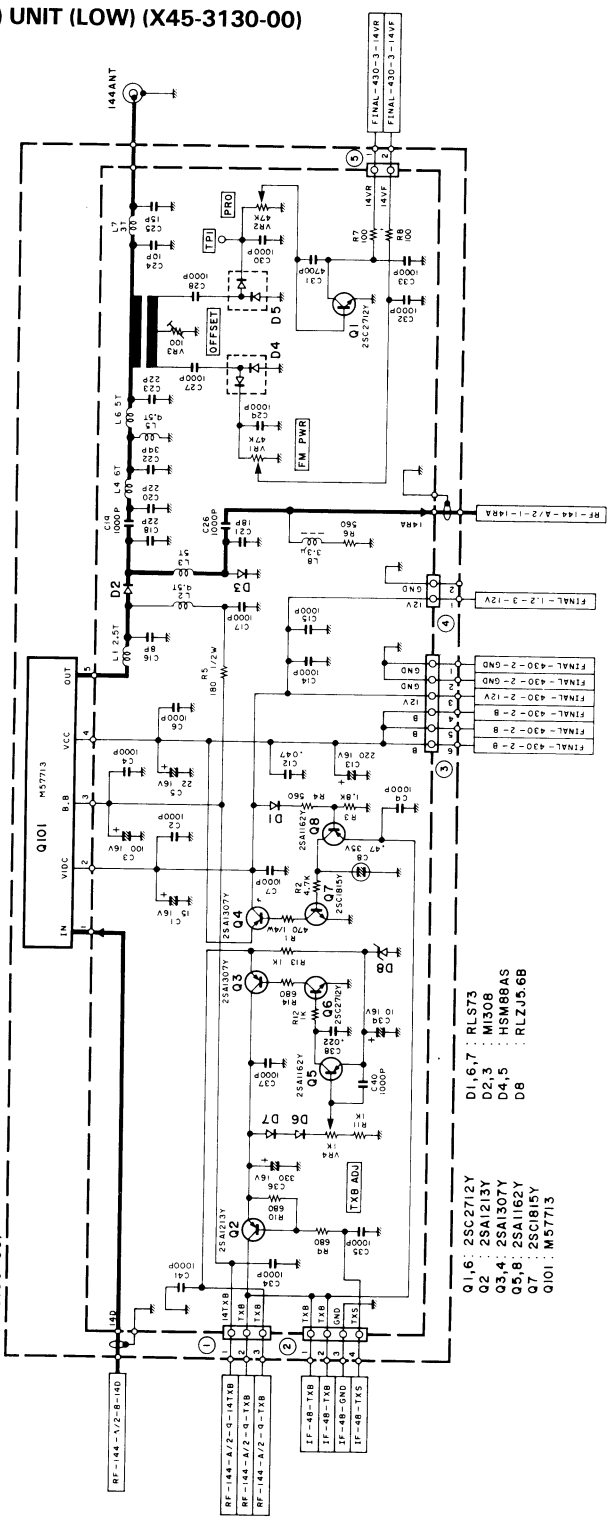




- IC 1 : μ PC1659G
- Q1, 8, 9 : 2SC4093
 Q2, 3 : 3SK184(R)
 Q4 : 2SK125
 Q5~7 : 2SC2712(Y)
 Q10, 12 : 2SC3098
 Q11, 13 : 2SC3357
 Q14 : FMC3
- D1, 12, 13 : RLZJ.5.6
 D2, 11 : ISV128
 D3~8, 16 : RLS135
 D9, 10, 14, 15 : HSM276S

Note: Circuit Diagram is subject to change without notice due to advancements in technology.

FINAL (144MHz) UNIT (LOW)
(X45-3130-00)

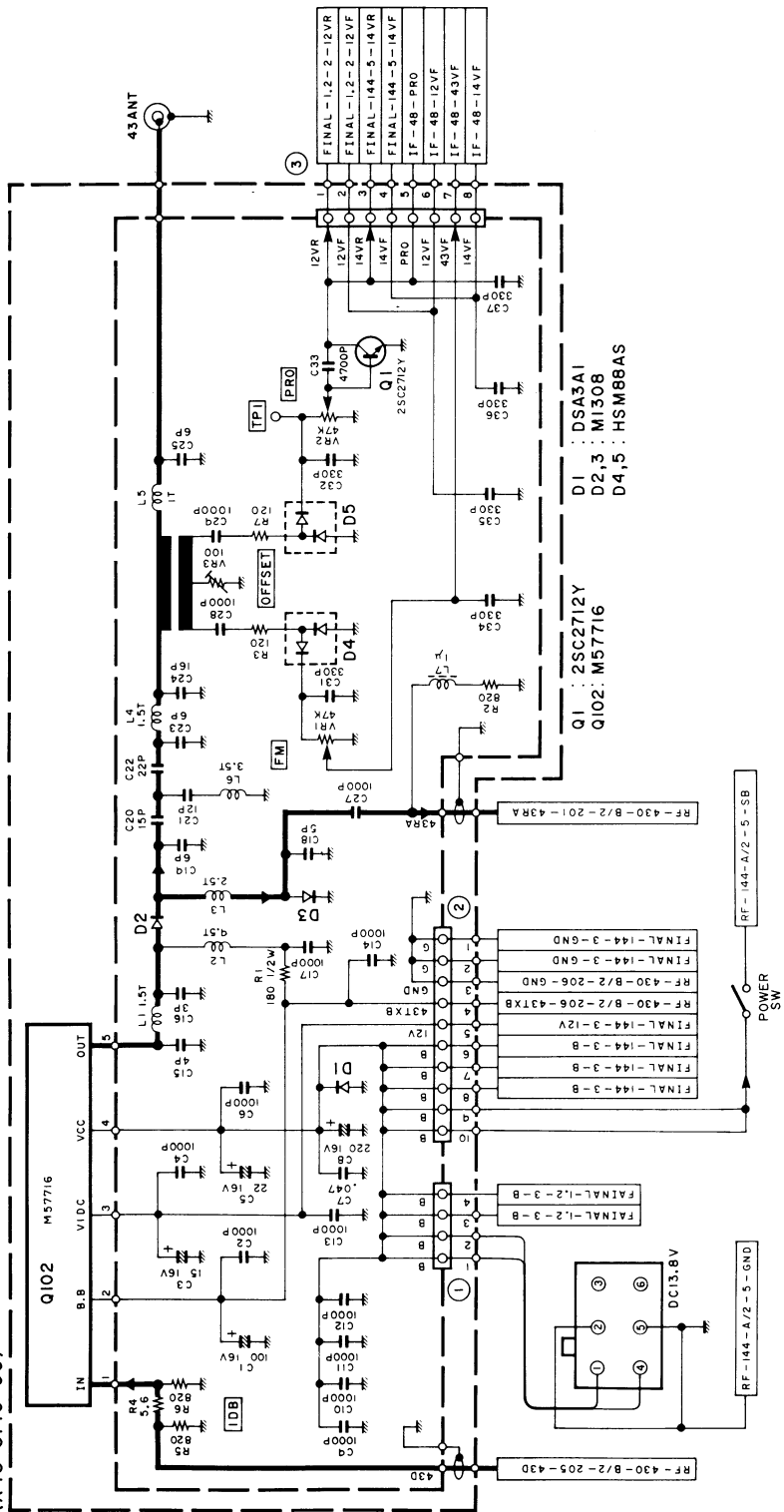


- Q1,6 : 2SC2712Y
- Q2 : 2SA1213Y
- Q3,4 : 2SA1307Y
- Q5,8 : 2SA1162Y
- Q7 : 2SC1815Y
- Q101 : M57713
- D1,6,7 : RL573
- D2,3 : M1308
- D4,5 : HSM88AS
- D8 : RLZJ5,6B

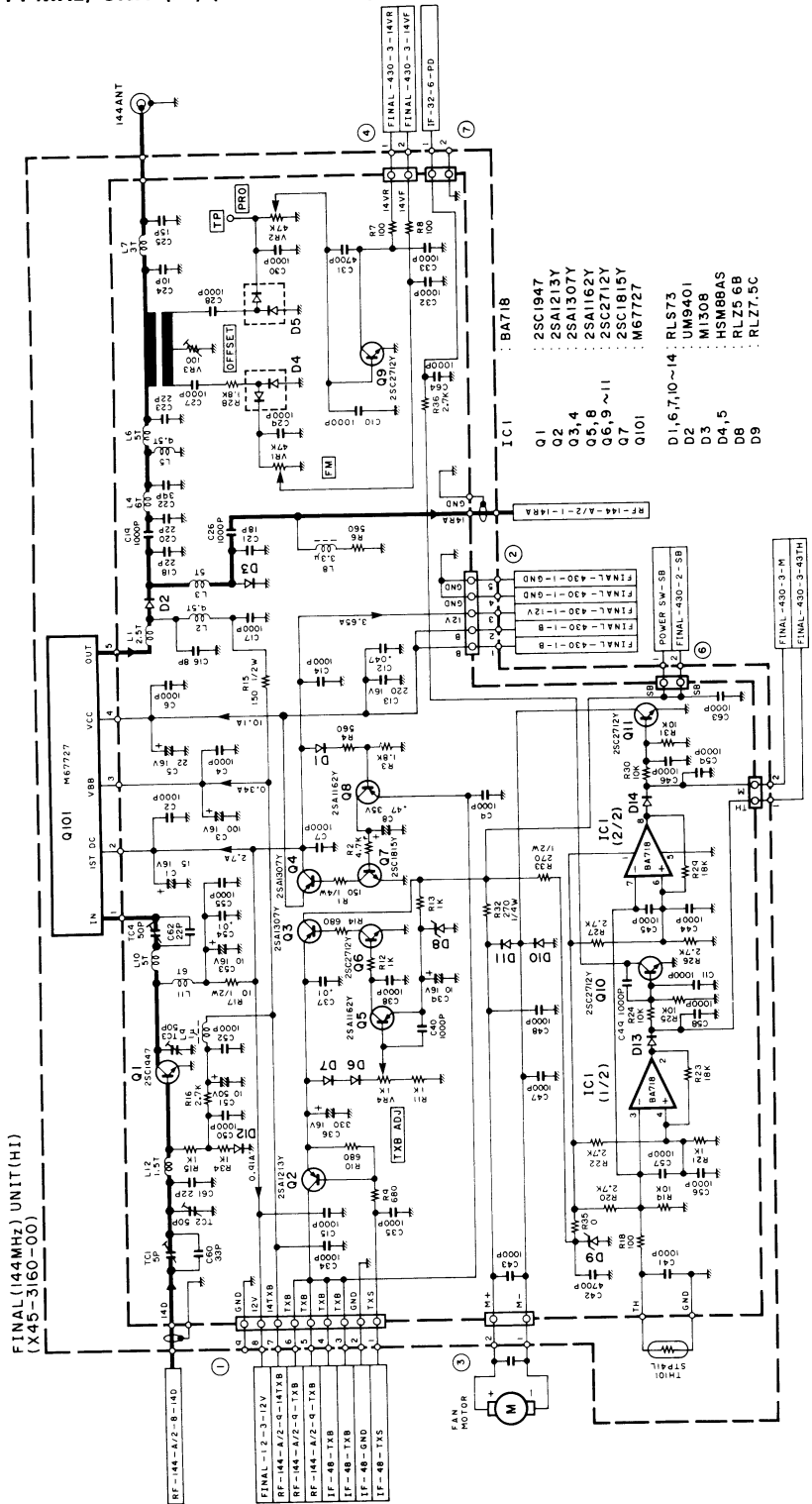
Note: _____
Circuit Diagram is subject to change without notice due to advancements in technology.

FINAL (430 MHz) UNIT (LOW) (X45-3140-00)

FINAL (430MHz) UNIT (LOW)
(X45-3140-00)

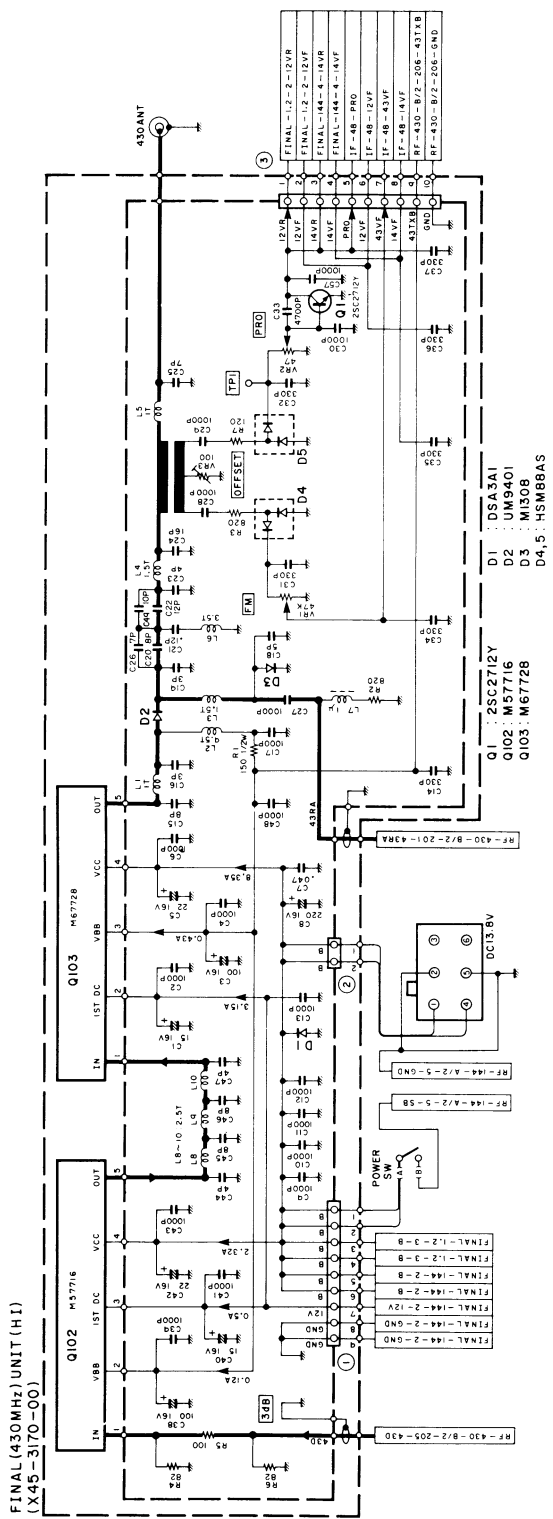


Q1 : 2SC2712Y
Q102: M57716
D1 : DSA3A1
D2,3 : MI308
D4,5 : HSM88AS



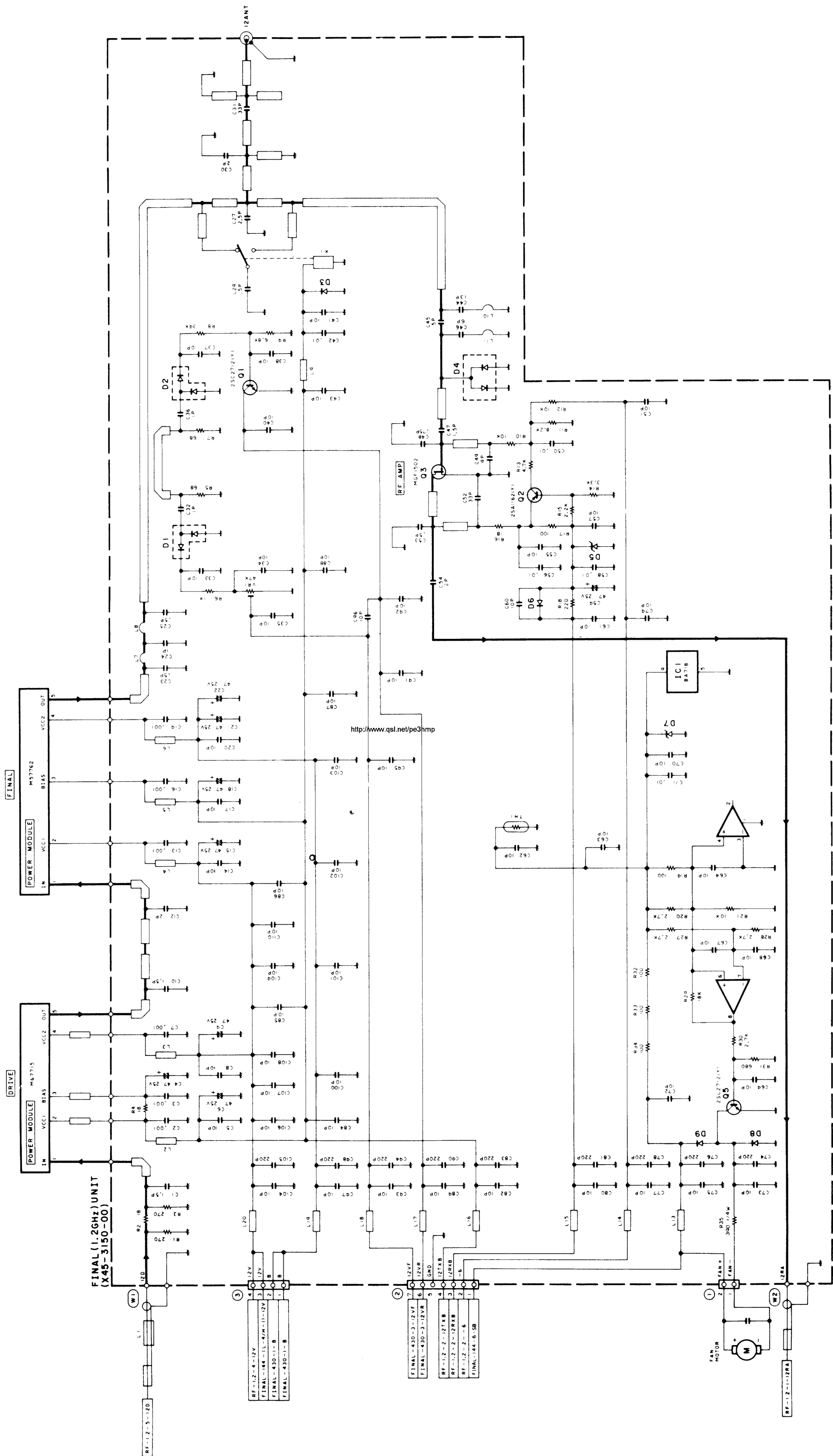
FINAL (144MHz) UNIT (HI)
(X45-3160-00)

Note: Circuit Diagram is subject to change without notice due to advancements in technology.



Note: Circuit Diagram is subject to change without notice due to advancements in technology.

FINAL (1.2 GHz) UNIT (X45-3150-00)



Note: Circuit Diagram is subject to change without notice due to advancements in technology.